Longchang Liu

Software Engineer

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github.com/leochame
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Education

Hangzhou Dianzi University Aug 2022 - Jun 2026
B.S. in Computer Science and Technology

Experience

Alibaba · *Amap* · *Intern* Oct 2025 - Jan 2026

Tencent · *Ads Marketing Services* · *Intern* Apr 2025 - Sep 2025

Open Source

LangChain4j (12k+ stars) contributor, contributing community fixes and capability improvements for the Java AI ecosystem.

Project Experience

Alibaba - Amap · Oct 2025 - Jan 2026
Responsible for productionizing the Order Analysis Assistant and AI Ride-Hailing LLM projects, improving complex task handling and integrating shared AI capabilities.

- Designed and launched the Deep Research capability for the Order Analysis Assistant, improving complex task decomposition and performance in long-context scenarios.
- Participated in the cold start of AI Ride-Hailing by building unified and stable AI invocation interfaces to support reliable integration on the core path.

Tencent - Ads ChatBI System · Apr 2025 - Sep 2025
Contributed to the NL2Chart core workflow of Tencent Ads ChatBI, enabling conversion from natural-language requests to charts.

- Designed and implemented an automated corpus review system to improve source data quality, raising recall accuracy on the core workflow from 50% to 80%. Introduced a state machine to improve system stability and crash recovery.

Technical Skills

- **AI**: Understanding of Transformer architecture, KV Cache, progressive disclosure, Agent context management, and common multi-agent collaboration patterns, with hands-on open-source practice.
- **Engineering**: SQL optimization, MySQL tuning, production troubleshooting, and JVM performance monitoring.
- **Computer Science**: Solid OS and networking fundamentals, including page tables, system calls, thread scheduling, TCP/UDP, and congestion control.
- **Java**: Strong understanding of JVM internals, class loading, memory model, and mainstream garbage collectors such as G1 and ZGC.
- **Concurrency**: Strong understanding of Synchronized, Volatile, AQS, and thread-pool implementation principles.